

LSI Logic

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Legal Department – IP
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Milpitas, CA 95035

Facsimile Transmittal LetterDate April 26, 2005Number of pages including cover sheet 3

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Application Number: 09/851,181
 Filing date: May 8, 2001
 First named inventor: Theodore Vaida et al.
 Attorney docket number: 01-035 / LSIL-01-035

Transmitted herewith for filing via facsimile:

- Information Disclosure Statement Letter
- Information Disclosure Statement by Applicant PTO/SB/O8A

Pursuant to 37 C.F.R. 1.8, I hereby certify that this correspondence is being transmitted by facsimile to the U.S. Patent and Trademark Office on the date indicated below:

<u>4/26/05</u>	<u>Connie del Castillo</u>	Connie del Castillo
Date	Signature	

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Theodore Vaida et. al.

Serial No. : 09/851,181

Filed : May 08, 2001

For : Application Specific Integrated
Circuit Having A Programmable
Logic Core And A Method Of
Operation Thereof

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Group Art Unit : 2661

Examiner : Phan, Tri H.

Atty Docket : LSIL-01-035 / 035

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

Connie Del Casullo

4/26/05 Connie M. Latta
Date Signature

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

The references listed in the attached form, copies of which are attached, may be material to examination of above-identified application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR 1.56 and 1.97.

It is requested that the information disclosed herein be made of record in the application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

If it is determined that any additional fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 12-2252.

LSI Logic Corporation
1621 Barber Lane, MS D-106
Milipitas, CA 95035
408-433-7475

Date: 4/25/2005

Respectfully submitted,

Henry Gold

Henry Groth

Reg. No. 39,696

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PTO/SB/08A (10-98)

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Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Sheet

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of

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Complete if Known

Application Number	09/851,181
Filing Date	May-08, 2001
First Named Inventor	Theodore Vaida
Group Art Unit	2661
Examiner Name	Tri H. Phan
Attorney Docket No.	LSIL-01-035 / 01-035

U.S. PATENT DOCUMENTS

Examiner Initials	Cite No.	U.S. Patent Document Number	Kind Code (if known)	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		6,701,432		Deng et al.	03-02-2004	
		6,347,347		Brown et al.	02-12-2002	
		6,393,457		Allison et al.	05-21-2002	
		6,226,292		DiPlacido	05-01-2001	
		6,205,509		Platko et al.	03-20-2001	
		6,807,581		Starr et al.	10-19-2004	

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Examiner signature		Date considered	
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PAGE 3/3 * RCVD AT 4/26/2005 12:55:46 PM [Eastern Daylight Time] * SVR:USPTO-EFXRF-1/3 * DNI:8729306 * CSID:4084337460 * DURATION (mm:ss):01-18